CALL FOR PAPERS

CASES 2014
International Conference on Compilers Architecture and Synthesis for Embedded Systems
New Delhi, India • October 12-17, 2014
http://www.esweek.org/cases

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CASES is the premier forum focused on compilers, architecture, and associated design methodologies for embedded systems. As part of Embedded Systems Week, CASES brings together researchers and practitioners to exchange information on emerging technologies and the latest advances in these areas. CASES uniquely aims to promote synergies across otherwise distinct communities in embedded systems. Previously unpublished papers containing novel ideas and significant technical results are solicited in the following areas:

• Architecture and Compilers for GPUs and accelerators
• Reconfigurable processors and FPGAs
• Compilation for reliability, power, performance
• System-on-chip architectures
• Multi-core and many-core processors for embedded computing
• Extensible and customizable processors
• On-chip communication architectures and networks-on-chip
• Memory system architecture and management
• Scratchpad, smart caches and compiler controlled memories
• Architectures for emerging nanoscale devices
• Programmable microfluidics
• 3D architectures, integration and synthesis
• Static and dynamic execution time analysis
• Specification of embedded systems
• Modeling & online management of reliability, power, performance
• Validation, verification & debugging of embedded software
• Embedded system design methodology
• Embedded system design space exploration
• Measurement Based Studies of Real Embedded Systems

Submissions should be in ACM proceedings format (9-point font, not exceeding 10 pages). The review process will be double blind, and submissions should not reveal the identities of authors. For further information, please see http://esweek.org/cases

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